

Bruce D. Shriver, Sr.

Resume

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SECTION TITLES

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University Adjunct, Affiliate, and Advisory Positions
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Professional Society Activities
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EXPERIENCE

Genesis 2, Inc.

November 1991 - present: President. Genesis 2, Inc. is a consulting firm involved in contract research and development, undertaking studies in core technologies, product design and implementation, project review and analysis, strategic and tactical planning, technology assessment and transfer activities, embedded systems design and implementation, and patent portfolio analysis and other intellectual property studies and issues.

University of Louisiana at Lafayette¹:

Vice-President for Research, ULL, and Eminent Scholar and Professor, Center for Advanced Computer Studies, 1989-1990. On-leave from those positions 1991-1993. The major responsibilities of the Vice-President for Research were to:

Articulate and move forward a credible and significant research, development, and technology transfer agenda for the university. Provide the leadership, vision, administrative

¹ Prior to 1999, the University of Louisiana at Lafayette (ULL) had been called the University of Southwestern Louisiana (USL).

and management skills, strategic and operating plans, resources and services to evolve the university's research, development, and technology transfer programs to a level of activity appropriate to the University's size, goals, and priorities.

Build strong, supportive alliances with local, regional, state, and national business, governmental, and industrial leaders that result in bringing funding, equipment, services and other resources to the University. Create relevant centers and institutes having specific research, development, and/or technology transfer objectives and build meaningful alliances between them.

Work to ensure the university provides a setting in which quality research can be nurtured, initiated, undertaken, and sustained. This included among other things: attracting and retaining quality faculty, students, and staff, supporting relevant masters and doctorate programs important to the University's research, development, and technology transfer agenda, providing a state-of-the-art research infrastructure and research facilities, and evolving a non-bureaucratic administrative strata.

D. H. Brown Associates, Inc.

October 1990 - October 1991: Director of Research and Chief Technical Officer. DHBA is a technology assessment, research, and consultancy firm involved in high performance systems, distributed and parallel computing, open systems and interoperability, and CAD, CAM, CAE, and CIM in mechanical engineering. The Director of Research and CTO is responsible for the content and direction of the technical programs of the firm, both in a strategic planning and a tactical sense.

University of Hawaii:

July 1988-June 1989: Director, Pacific Research Institute for Information Systems and Management (PRIISM); Henry A. Walker, Jr. Distinguished Professor; Professor, Department of Decision Sciences; Professor, Department of Information and Computer Sciences. The major responsibilities of the Director of PRIISM were to:

Provide the vision, technical leadership, management skill, and financial administration to develop and run an internationally recognized institute in information systems and technology.

Develop an integrated approach to information science and technology activities at UH from both a research and curriculum perspective by building linkages among the departments focusing on decision sciences, computer science, computer engineering, communications, library science, information systems, and departments needing to exploit these technologies.

Work with universities, corporations, and governments across Asia and the Pacific to establish a computer and information industry hub in Hawaii. Link university programs in information systems and technology at UH to the business community in ways that would help establish and enhance a computer and information industry in Hawaii.

IBM Thomas J. Watson Research Center:

Yorktown Heights, New York: July 1986-April 1987, Department Group Manager, Software Technology Department; June 1984- June 1988, Research Staff Member; June 1985-July

1986, Senior Manager, Software Technology; December 1984-June 1985, Senior Manager, Knowledge-Based System Architectures and Applications.

Department Group Managers, DGMs, in the Computer Science Department at T. J. Watson Research Center report directly to the Vice-President of Systems of the Research Division. The Software Technology DGM position was responsible for providing technical leadership and management for several departments doing basic and applied research in software and systems technology. The department had projects in Integrated Design and Programming Environments, Language Design and Language Implementation Technology, Alternative Programming Paradigms, Object-Oriented Technology, Applications Enabling Technologies, and other software technology areas.

DGMs are responsible for developing an environment, strategy, and operating plan that set the context and rationale for undertaking and supporting these activities. The department goals were to conduct research and development activities that would (a) be of value to IBM's product and development divisions and (b) have the department internationally recognized by peer groups as a leading center of advanced work in software and systems, both internally and external to the corporation. DGMs develop significant interactions with IBM divisions that focus on and impact both their short term and longer-term technical needs by initiating joint programs and projects. DGMs regularly evaluate the technical and management contributions and accomplishments made by the people in their departments as measured against the above goals and initiating changes in the group structure as required to meet these goals.

DGMs evaluate new and emerging technologies and initiates projects in potentially important areas. DGMs participate in task forces, technical audits, and inter-divisional workshops and seminars to study and make recommendations on such technologies and projects and make presentations to both Division and Corporate Level IBM Executives giving a coherent and cohesive view of the technology for which the DGM has responsibility. DGMs chair or are members of Corporate Level or Inter-Divisional bodies, such as the Executive Tools Council of which I was a member. This council had the responsibility for developing a strategy and plan for internal software development tools within the corporation.

DGMs work quite closely with the managers that report to them to identify and nurture individuals that are likely to develop into key technical and management leaders. The entire management team develops and regularly reviews career development plans for all employees.

DGMs administer the budget for the departments that report to them. Typical budget areas include operating expense, capital equipment, inter-divisional contracts, laboratory facilities and computer center usage, travel, materials, colloquiums, faculty and student visitors and summer positions, etc. DGMs are directly involved in the recruiting of high quality people across the needs of their departments; thus, a DGM cultivates interactions with key universities and professors and their students and keeps up-to-date on their results. They acquire, distribute, and administer head count to their departments as appropriate.

University of Louisiana at Lafayette:

Computer Science Department: August 1981-May 1984, Alfred Lamson Research Professor of Computer Science. August 1977-1981, Professor of Computer Science. August 1973-

1977, Associate Professor of Computer Science. Responsible for conducting research, organizing courses, and lecturing in computer systems architecture, operating systems, and programming languages and systems.

University of Århus (Denmark):

Computer Science Department: August 1971-July 1973, Visiting Senior Faculty Member engaged in research and offering seminars in computer design for scientific computation, microprogramming, and numerical analysis. Led the architecture team that designed the Mathilda horizontally microprogrammed processors.

National Science Foundation:

June 1969-June 1971: National Science Foundation Fellow (two-year tenure for independent study while pursuing doctorate degree).

State University of New York at Buffalo:

Department of Computer Science: September 1968-June 1969, Research Assistant working with faculty members in numerical analysis research.

California State Polytechnic University at Pomona:

Aerospace Engineering Department: January, 1964-September, 1968, Instructor responsible for organizing courses and lecturing in various applied mechanics, mathematics, and computer science courses.

National Aeronautics and Space Administration:

Moffett Field, California: August 1963 to October 1963, Research Engineer, Ames Research Center, Engaged in the analysis of subsystems used in the biosatellite project.

UNIVERSITY ADJUNCT, AFFILIATE, and ADVISORY POSITIONS

University of Hong Kong, Hong Kong, China

2003-present, Honorary and Visiting Professor Department of Computer Science and Information Systems

1999-2003, Honorary and Visiting Professor, Faculty of Engineering

University of Tromsø, Norway, Department of Computer Science:

1999-present, Professor-at-Large

1993-present, Adjunct Professor

University of Texas at Austin, Department of Electrical and Computer Engineering:

1992-1995, Visiting Committee

New Mexico State University, Department of Computer Science:

1987-1992, Board of Advisors

Oregon State University, Department of Computer Science:

1984-1987, Adjunct Professor

Technical University of Berlin, Department of Computer Science:

1982-1984, Affiliate Scientific Staff Member of the Computer Architecture and Microprogramming (CAMP) Research Group

University of Louisiana at Lafayette, Electrical and Computer Engineering Department:
1982-1984, Adjunct Professor.

DEGREES

Ph.D., Computer Science, State University of New York at Buffalo, Amherst, New York, September 1971.

M.Sc., System Analysis, West Coast University, Los Angeles, California, November, 1967; M.Sc., Systems Engineering, May 1968.

B.S., Aerospace Engineering, and B.S., Mathematics, California State Polytechnic University, Pomona, California, June 1963.

CONSULTING

Consulted for, presented invited lectures at, or conducted intensive courses for a number of firms. Among them are AMD (Advanced Micro Devices), IBM's General Products Division and Office Products Division in Boulder, Colorado, and San Jose, California, IBM's Systems Research Institute in New York, Digital Equipment Corporation, Texas Instruments, Intel Corporation, Hitachi, Ltd. (Japan), National Semiconductor, NCR, RCA, GMD (Germany), Daimler-Chrysler Research (Palo Alto), Raza Foundries, PRISMedia Networks, Inc., Omnishift Technologies, Inc., Pacific Regent Management, Bristows (London), Fish & Neave, Pillsbury, Madison & Sutro, Sonnenschein Nath & Rosenthal, Bilzin Sumberg Dunn Price & Axelrod LLP, Scott, Ballenger & Moore Co. L.P.A., Nixon & Vanderhye P.C., Fulbright & Jaworski, O'Melveny & Myers, LLP, Paul, Hastings, Janofsky & Walker LLP., Morgan Lewis and Bockius, Greenbaum, Rowe, Smith, Ravin, Davis & Himmel, LLP, Sierra Systems (Canada), and Medical Information Systems, Inc.

SELECTED PUBLICATIONS

The Anatomy of a High-Performance Microprocessor, A Systems Perspective, Bruce Shriver and Bennett Smith, IEEE Computer Society Press, July 1998 (2nd printing, April 1999).

Programming Models for Massively Parallel Computers, Eds. W. Giloi, S. Jahnichen, and B. Shriver, IEEE Computer Society Press, September 1995.

Programming Models for Massively Parallel Computers, Eds. W. Giloi, S. Jahnichen, and B. Shriver, IEEE Computer Society Press, September 1993.

Declarative Systems, Eds. G. David, R. T. Boute, and B. Shriver, North Holland, 1990.

Visualization in Scientific Computing, Eds. G. Nielson and B. Shriver, IEEE Computer Society Press, 1990.

Research Issues in Object-Oriented Programming, Eds. B. Shriver and P. Wegner, MIT Press, October, 1987.

Methodologies for Computer System Design, Eds. W. Giloi and B. Shriver, North-Holland Co., Amsterdam, 1985.

"A Flexible Model for Studying the Execution of Dataflow Programs in Distributed Systems," L. Delcambre and B. Shriver, **Interfaces in Computing**, Vol. 3, No.1, February 1985.

"Recent Developments in Firmware Engineering," S. Dasgupta and B. Shriver, in **Advances in Computers**, Vol. 24, Ed. M. Yovits, Academic Press, Inc. New York, 1985.

"An Overview of the Computer System Design Process," W. Giloi and B. Shriver, in **Methodologies for Computer System Design**, Eds. W. Giloi and B. Shriver, North-Holland Co., Amsterdam, 1985.

"A Methodology for Designing and Modeling Reconfigurable Systems," V. Srinivasan and B. Shriver, **International Journal of Computer and Information Sciences**, Vol. 13, No. 5, October 1984.

"A Message Repository Definitional Facility: An Architectural Model for Interprocess Communication," K. Kavi, E. Banios, and B. Shriver, Proc. of the 11th Annual International Symposium on Computer Architecture, Ann Arbor, Michigan, June 1984.

"Microcode Compaction: Extending the Boundaries," D. Landskov, J. Fisher and B. Shriver, **International Journal of Computer and Information Sciences**, Vol. 13, No. 1, February, 1984.

"Variable Precision and Interval Arithmetic: A Portable Enhancement to Fortran," J. Jeter and B. Shriver, **Advances in Engineering Software**, Vol. 6, No. 1, January 1984.

"An Introduction to DEA-Suite: A Distributed Execution Analysis Suite," L. Delcambre, C. Linn, B. Shriver, and S. Landry, Proc. of the Seventeenth Hawaii International Conference on Systems Sciences, Honolulu, Hawaii, January 1984.

"A Resource Request Model for Microcode Compaction," T. Larsen, D. Landskov and B. Shriver, Proc. of the Sixteenth Annual Microprogramming Workshop, Downingtown, Pennsylvania, October 1983.

"Component Identification for a Portable, Retargetable Firmware Development System," J. Linn, B. Shriver and S. Dasgupta, Proc. of the IEEE International Workshop on Computer Systems Organization, New Orleans, Louisiana, March 1983.

"Dataflow," B. Shriver, S. Landry and V. Srinivasan, in **Encyclopedia of Computer Science and Engineering**, Second Edition, Ed. A. Ralston, Van Reinhold Co., New York, 1983.

"A Distributed Operating Systems Kernel Based on Dataflow Principles," M. Measures, P. Carr and B. Shriver, Proc. of COMPCON Fall '82 Conference, Washington, D.C., September 1982.

"Firmware Quality Assurance," H. Berg, P. Roa and B. Shriver, Proc. of the National Computer Conference, NCC '82, AFIPS Press, Houston, Texas, May 1982.

"Some Experiments in Local Microcode Compaction for Horizontal Machines," S. Davidson, D. Landskov, B. Shriver and P. Mallett, **IEEE Transactions on Computers**, Vol. C-30, No. 7, July 1981.

"Microcode Compaction: Looking Backward and Looking Forward," J. Fisher, D. Landskov and B. Shriver, Proc. of the National Computer Conference, NCC '81, AFIPS Press, Chicago, Illinois, May 1981.

"Specifying Target Resources in a Machine Independent Higher Level Language," S. Davidson and B. Shriver, Proc. of the National Computer Conference, NCC '81, AFIPS Press, Chicago, Illinois, May 1981.

"Firmware Engineering: Methods and Tools for Firmware Specification and Design," W. Giloi, R. Gueth and B. Shriver, Proc. of the National Computer Conference, NCC '81, AFIPS Press, Chicago, Illinois, May 1981.

"Abstracts Dataflow Protocol for Communication in Distributed Computer Systems," V. Srinivasan and B. Shriver, Proc. of the Conference on Distributed Computing, COMPCON '80 Proceedings, September 1980.

"Local Microcode Compaction Techniques," D. Landskov, S. Davidson, B. Shriver and P. Mallett, **ACM Computing Surveys**, Vol. 12, No 3, September 1980.

"Marble: A High Level Machine Independent Language for Microprogramming," S. Davidson and B. Shriver, in **Firmware, Microprogramming, and Restructurable Hardware**, Eds. G. Chroust and J. Muehlbacher, North-Holland Co., Amsterdam, 1980.

"Firmware Engineering: An Extensive Update," S. Davidson and B. Shriver, in **Firmware, Microprogramming, and Restructurable Hardware**, Eds. G. Chroust and J. Muehlbacher, North-Holland Co., Amsterdam, 1980, and in **Informatik Fachberichte, Vol. 31: Firmware Engineering**, Springer-Verlag, Berlin, 1980.

"A Simulation Environment for Performing Dataflow Research," S. Landry and B. Shriver, Proc. of the Conference on Simulation, Measurement and Modeling of Computer Systems, Boulder, Colorado, August 1979.

"Simulated Execution of Dataflow Programs on Processors Having Finite Resources," D. Langan and B. Shriver, Proc. of the Conference on Simulation, Measurement and Modeling of Computer Systems, Boulder, Colorado, August 1979.

"An Overview of Dataflow Related Research at the University of Louisiana at Lafayette," B. Shriver and S. Landry, Proc. of the Toulouse Dataflow Conference (Langages a Machines Cadencees par Les Donnes, Eds. J. Syre and D. Compte), ONERA CERT, Toulouse, France, February 1979.

"A Dataflow Simulation Research Environment," S. Landry and B. Shriver Proc. of the Toulouse Dataflow Conference (Langages a Machines Cadencees par Les Donnes, Eds. J. Syre. and D. Compte), ONERA CERT, Toulouse, France, February 1979.

"Virtual Machines Monitors," B. Shriver, D. Hyams and J. Anderson in **Encyclopedia of Computer Science and Technology**, Eds. W. Belzer, A. Holzman and A. Kent, Marcel Dekker, Inc., New York, 1979.

"Virtual Memory Systems," J. Anderson and B. Shriver in **Encyclopedia of Computer Science and Technology**, Eds. W. Belzer, A. Holzman and A. Kent, Marcel Dekker, Inc., New York, 1979.

"Some Experiments Using Interval Arithmetic," E. Reuter, J. Jeter, J. Anderson and B. Shriver, Proc. of the Fourth IEEE Symposium on Computer Arithmetic, Santa Monica, California, October 1978.

"Computer Systems Organization," H. Apfelbaum, R. Case, B. Shriver, H. Stone and W. Thornton, **IEEE Computer**, Vol. 11, No. 9, September 1978.

"An Overview of Firmware Engineering," S. Davidson and B. Shriver, **IEEE Computer**, Vol. 11, No. 5, May 1978.

"A Mathematical Model for a Virtual Machine Monitor and Its Supportive Structure," D. Nguyen, J. Anderson, B. Shriver and R. Michelsen, Proc. of the Eleventh Hawaii International Conference on System Sciences, Honolulu, Hawaii, January 1978.

"A Unified Numeric Representation Arithmetic Unit and Its Language Support," P. Kornerup and B. Shriver, **IEEE Transactions on Computers**, Vol. C-26, No. 7, July 1977.

"A Virtual Machine Monitor for A Distributed Minicomputer System," B. Shriver, J. Anderson and D. Hyams, Proc. of the Tenth Hawaii International Conference on System Sciences, Honolulu, Hawaii, January 1977.

"An Implementation Scheme for a Virtual Machine Monitor to be Realized on User-Microprogrammable Minicomputers," B. Shriver, J. Anderson, L. Waguespack, D. Hyams and R. Bombet, Proc. of ACM '76 national Conference, Houston, Texas, October 1976.

"The UNRAU, a Unified Numeric Representation Arithmetic Unit," B. Shriver and P. Kornerup, Proc. of the Third IEEE Symposium on Computer Arithmetic, Dallas, Texas, November 1975.

"A Bibliography on Computer Arithmetic," B. Shriver and E. Reuter, Proc. of the Third IEEE Symposium on Computer Arithmetic, Dallas, Texas, November 1975.

"The Design of a Polymorphic Arithmetic Unit," A. Lang and B. Shriver, Proc. of the Third IEEE Symposium on Computer Arithmetic, Dallas, Texas, November 1975.

"An Integrated Approach to the Design of Fault Tolerant Computing Systems," P. Caillouet and B. Shriver, Proc. of the Seventh Annual Micro-programming Workshop, Palo Alto, California, September 1974.

"Systems Design for Scientific for Scientific Computation," B. Shriver, Proc. of the ACM International Computing Symposium, Davos, Switzerland, September 1973.

"Interpolation Formulas of Gauss Type for Approximate Solution of the N-dimensional Heat Equation," B. Shriver, Ph.D. Dissertation, Department of Computer Science, State University of New York at Buffalo, Amherst, New York, September 1971.

"Microprogramming and Numerical Analysis," B. Shriver, **IEEE Transactions on Computers**, Vol. C-21, No. 7, July 1971.

"Permanent Function of a Square Matrix," Algorithm 361, B. Shriver, P. Eberlein, and R. Dixon, **Communications of the ACM**, Vol. 17, No. 11, November 1969.

REPORTS

"Functional Specifications for DEA-Suite: A Distributed Execution Analysis Suite," L. Delcambre, C. Linn, B. Shriver and S. Landry, Computer Science Department Report No. 83-3-2, University of Louisiana at Lafayette, August 1983.

"Generalized Cost Equations for Relational Algebra Operators," E. Adams, C. Lillie, S. Nemecek, and B. Shriver, Computer Science Department Report No. 83-6-1, University of Louisiana at Lafayette, May 1983.

"CODES - A Program Development Suite for Horizontal Microarchitectures," J. Linn, B. Shriver and S. Dasgupta, Computer Science Department Report No. 83-3-1, University of Louisiana at Lafayette, January 1983.

"Microcode Compaction: The State of the Art," J. Fisher, D. Landskov and B. Shriver, Computer Science Department Report No. 82-3-3, University of Louisiana at Lafayette, January 1982.

"A Methodology for Designing and Modeling Reconfigurable Systems," B. Shriver and V. Srin, Computer Science Department Report No. 81-3-3, University of Louisiana at Lafayette, August 1980.

"Extensions to the Dataflow Simulation System for Supporting Extended Dataflow Models," S. Landry and B. Shriver, Computer Science Department Report No. 80-3-7, University of Louisiana at Lafayette, July 1980.

"Some Experiments in Local Microcode Compaction," S. Davidson, D. Landskov, B. Shriver and P. Mallett, Computer Science Department Report No. 80-3-10, University of Louisiana at Lafayette, 1980.

"Local Microcode Compaction Techniques," D. Landskov, S. Davidson, B. Shriver, and P. Mallett, Computer Science Department Report No. 80-3-2, University of Louisiana at Lafayette, 1980.

"Implementation and Evaluation of Interval Arithmetic Software: Report 2, the Honeywell Multics System," K. S. Podlaska-Lando, E. Rueter and B. Shriver, Technical Report 0-79-1, U.S. Army Engineer Waterways Experiment Station, Vicksburg, Mississippi, April 1979.

"Selected Reprints from "Computing in the 1980's", by T. Lewis and B. Shriver, Computer Science Department Report No. 79-3-7, University of Louisiana at Lafayette, 1979.

"A User's Guide to the Dataflow Simulator," S. Landry and B. Shriver. Computer Science Department Report No. 78-3-2, University of Louisiana at Lafayette, 1978.

"A 56 Decimal Digit Implementation of an Interval Arithmetic Package on the Multics System," E. Reuter, J. Jeter, J. Anderson and B. Shriver, Computer Science Department Report No. 77-7-1, University of Louisiana at Lafayette, November 1977.

"More Effective Teaching," G. Abshire and B. Shriver, Computer Science Department Report No. 77-2-1, University of Louisiana at Lafayette, August 1977.

"A Dataflow Simulator System and Its Use," S. Landry and B. Shriver, Computer Science Department Report No. 77-3-2, University of Louisiana at Lafayette, 1977.

"The Implementation of the Mathematics Research Center's Interval Arithmetic Package on the Multics System," E. Reuter, S. Podlaska-Lando and B. Shriver, Computer Science Department Report No. 76-7-2.A, University of Louisiana at Lafayette, August 1976.

"An Implementation Scheme for a Virtual Machine Monitor to be realized on User-Microprogrammable Minicomputers," B. Shriver, J. Anderson, D. Hyams, L. Waguespack, and R. Bombet, Computer Science Department Report No. 76-5-1, University of Louisiana at Lafayette, 1976.

"A Description of the Mathilda Processor," B. Shriver and P. Kornerup, Computer Science Department Report DAIMI PB-52, University of Århus, Århus, Denmark, September 1975.

"Integrated Research Projects in Virtual Computer Systems: Project Beta," B. Shriver, T. Lewis, and J. Anderson, Computer Science Department. Report No. 75-2-0, Computer Science Department, University of Louisiana at Lafayette, June 1975.

"Integrated Research Projects in Virtual Computer Systems: Project Beta," B. Shriver and T. Lewis, Computer Science Department Report No. 74-3-1, University of Louisiana at Lafayette, 1974.

"An Overview of the Mathilda Systems," P. Kornerup and B. Shriver, SIGMICRO Newsletter, Vol. 5, No. 1, January 1975.

"Toward Reasonability in CPU Design: A Case Study," B. Shriver and R. Rosin, presented at the Workshop on Education and Computer Architecture, ACM '73, Atlanta, Georgia, August 1973.

"A Small Group of Research Project in Machine Design for Scientific Computation," B. Shriver, Department of Computer Science Report No. PB-14, University of Århus, Denmark, June 1973.

"Some Interpolation Formulas for Approximate Solution to the Heat Equation," B. Shriver, Department of Computer Science Report No. PB-17, University of Århus, Denmark, June 1973.

"A Description of the Mathilda System," B. Shriver, Department of Computer Science Report No. PB-13, University of Århus, Denmark, April 1973.

"Computer Sizing of Fluid Elements: An Interactive Computer Approach", B. Shriver, presented at the Southern California Meter Association Short Courses, Harbor College, Los Angeles, California, April 1968.

"An Application of the Slab Analogy in Determining the Thermal Stresses in Solid Propellant Rocket Motor Grains," B. Shriver, Aerospace Engineering Department, California State Polytechnic University, Pomona, California, June 1963.

EDITORIAL POSITIONS

Editorial Board, **IEEE Computational Science & Engineering**, January 1993-December 1995.

Editorial Board, **IEEE Spectrum**, January 1992- December 1993.

Editor-in-Chief, Computer, January 1987-December 1990.

Innovative Technology Book Series for Prentice Hall, Editors: D. Allison (Stanford), D. Farber (University of Pennsylvania), and B. Shriver. Some titles in this series are:

A VHDL Primer (Bhasker)

Mathematics: A Practical Approach (Blachman)

Digital Design Using Field Programmable Gate Arrays (Chan et al)

Task Scheduling in Parallel and Distributed Systems (El-Rewini et al)

Designing with FPGAs and CPLDs (Jenkins)

Superscalar Microprocessor Design (Johnson)

MIPS RISC Architecture (Kane et al)

Zen and the Art of the Internet: A beginner's Guide (Kehoe)

Parallel Processing in Industrial Real Time Applications (Lawson)

Systems Programming with Modula-e (Nelson et al)

Open Systems (Nutt)

The Internet Message: Closing the Book with Electronic Mail (Rose)

The Little Black Book: Mail Bonding with OSI Directory Support (Rose)

The Open Book: A Practical Perspective on OSI (Rose)

The Simple Book: An Introduction to the Management (Rose)

A C++ Toolkit (Shapiro)

Microprocessor-Based Design (Slater)

The SPARC Architecture Manual, Version 8 (SPARC International)

The SPARC Architecture Manual, Version 9 (SPARC International)

Hermes: A Language for Distributed Computing (Strom et al)

Designing State Machine Controllers Using Programmable Logic (Tresler)

Designing Object-Oriented Software (Wirfs-Brock et al)

Special Issue on Parallel and Distributed Systems, Eds. H. El-Rewini, T. Lewis, and B. Shriver, **IEEE Parallel & Distributed Technology**, Vol. 1, No. 3, August 1993.

IEEE Computing Futures, Editorial Advisory Committee, Winter, 1989.

Special Issue on Artificial Neural Systems, Ed. B. Shriver, **IEEE Computer**, Vol. 21, No. 3, March 1988.

Editor-in-Chief, IEEE Software, January 1983-December 1986.

SIGPLAN Notices, Vol. 21, No 10, October 1986, Eds. B. Shriver and P. Wegner.

Proceedings of the Annual Hawaii International Conference on System Sciences, IEEE Computer Society Press, Washington, D. C.:

Vol. II of HICSS-29, Software Technology, Eds. H. El-Rewini, and B. Shriver, 1996; Vol. I of HICSS-28, Architecture, Eds. T. M. Mudge and B. Shriver, 1995; Vol. II of HICSS-28, Software Technology, Eds. H. El-Rewini, and B. Shriver, 1995; Vol. I of HICSS-27, Architecture, Eds. T. M. Mudge and B. Shriver, 1994; Vol. II of HICSS-27, Software Technology, Eds. H. El-Rewini, and B. Shriver, 1994; Vol. I of HICSS-25, Architecture and Emerging Technologies, Eds. V. Milutinovic and B. Shriver, 1992; Vol. II of HICSS-26, Software Technology, Eds. H. El-Rewini, T. Lewis, and B. Shriver, 1993; Vol. II of HICSS-25, Software Track, Ed. B. Shriver, 1992; Vol. I of HICSS-24, Architecture and Emerging Technologies, Eds. V. Milutinovic and B. Shriver, 1991; Vol. II of HICSS-24, Software Track, Ed. B. Shriver, 1991; Vol. II of HICSS-23, Software Track, Ed. B. Shriver, 1990; Vol. II of HICSS-22, Software Track, Ed. B. Shriver, 1989; Vol. II of HICSS-21, Software Track, Ed. B. Shriver, 1988.

Selected Papers in Software, Western Periodicals Company, North Hollywood, California:

Vol. II of HICSS-20, Ed. B. Shriver, 1987, Vol. II of HICSS-19, Ed. B. Shriver, 1986, Vol. II of HICSS-18, Ed. B. Shriver, 1985.

Selected Papers in Medical Information Processing, Western Periodicals Company, North Hollywood, California:

Vol. II of HICSS-17, Eds. Cousins, Walker, Shriver, & Sprague, 1984; Vol. II of HICSS-16, Eds. Shriver, Cousins, Walker, & Sprague, 1983; Vol. II of HICSS-15, Eds. Shriver, Walker, Grams, & Sprague, 1982; Vol. II of HICSS-14, Eds. Shriver, Walker, Grams, & Sprague, 1981; Vol. III of HICSS-13, Eds. Shriver & Sprague, 1980; Vol. III of HICSS-12, Eds. Shriver, Walker, & Sprague, 1979.

Guest Editor, **Journal of Medical Systems**:

Vol. 7, Numbers 4/5, 1983, Vol. 6, Numbers 1/4/5, 1982, Vol. 5, Number 4, 1981, Vol. 4, Number 2, 1980, Vol. 3, Numbers 1/2, 1979.

Computer Systems Editor, **Medical Electronics**, 1981-1987.

Computer Systems Editor, **Measurements and Control**, 1981-1987

Special Issue on Microprogramming Tools and Techniques, Guest Eds. T. Lewis and B. Shriver, **IEEE Transactions on Computers**, Vol. C-30, No. 7, July, 1981.

Editorial Board, **Journal of Medical Systems**, 1980-1987.

Associate Technical Editor, **IEEE Computer**, 1978-1983.

Special Issue on the Computing Problems of the 1980's, Guest Eds. T. Lewis and B. Shriver, **IEEE Computer**, Vol. 11, No. 9, September 1978.

Microcomputer and Minicomputer Systems, Eds. R. Eckhouse and B. Shriver, Vol. III of HICSS-11, Western Periodicals Company, North Hollywood, California, 1978.

Special Issue on Firmware Engineering, Guest Ed. B. Shriver, **IEEE Computer**, Vol. II, No. 5, May 1978.

Microprocessors, Microprogramming, and Minicomputers, Eds. R. Chattergy, U. Pooch, T. Lewis, and B. Shriver, Western Periodicals Company, North Hollywood, California, 1977.

Editor, **ACM SIGMICRO Newsletter**, 1975-1977.

GRANTS

RCA, Project CODES - A Program Development suite for Horizontal Micro-programmable Architectures, Co-Principal Investigator with J. Linn and S. Dasgupta, \$277,010, August 1982 through December 1983.

RCA, D-Suite - Specification of a Set of Tools to Study the Decomposition, Scheduling, and Execution of Programs in Distributed Execution Environments, Co-Principal Investigator with L. Delcambre, C. Linn, and S. Landry, \$48,000, March 1983 through August 1983.

Texas Instruments, Inc. Software Research and Development Agreement 3309, Internal System Software for a New Generation TI Personal Computer, Co-Principal Investigator with J. Linn and C. Linn, \$54,300, August 1982 through May 1983.

Phoenix Computer Graphics, Inc., Instructional, Research and Development Activities Involving Small Networks of Graphics Work Stations, Co-Principal Investigator with T. Cousins and J. Linn, \$156,000, June 1982 through July 1983.

Texas Instruments, Inc. Software Research and Development Grant D3151 Extension, Message Repositories and Architectures, Principal Investigator, \$60,000, June 1981 through May 1982.

Texas Instruments, Inc. Hardware Grant, Principal Investigator, \$116,235, June 1981 through May 1982.

Texas Instruments, Inc. Software Research and Development Grant D3151 Extension, Message Repositories and Architectures, Principal Investigator, Software and Hardware, \$42,285, June 1981 through May 1982.

Texas Instruments, Inc. Software Research and Development Grant D3151 Extension, Kernels for Distributed Operating Systems, Co-Principal Investigator with P. Carr, \$82,095, October 1980 through May 1981.

Texas Instruments, Inc. Software Research and Development Grant D3151 Extension, Kernels for Distributed Operating Systems, Co-Principal Investigator with P. Carr, Software and Hardware, \$31,000, September 1980.

Texas Instruments, Inc. Software Research and Development Grant D3151, Kernels for Distributed Operating Systems, Co-Principal Investigator with V. Srimi, \$358,000, October 1979 through January 1981.

U. S. Army Research Office Grant DAAG29-78-G-0068, Variable Precision and Interval Arithmetic, Principal Investigator, \$16,374, March 1979 through February 1980. (Grant extended by WES Contract No. DACW39-80-M-2560, \$3000, April 1980 through July 1980.)

National Science Foundation Grant MCS77-27824, Workshop on Computing Problems of the 1980's, Co-Principal Investigator with T. Lewis (Oregon State University), \$6,000, March 1978 through September 1978. (A Textronix grant for \$1000 was also issued in conjunction with this meeting.)

U.S. Army Research Office Grant DAAG29-78-G-0068, Variable Precision Interval Arithmetic, Principal Investigator, \$16,000, March 1978 through February 1979.

U. S. Army Corps of Engineers DACA39-77-M-106, Studies in High Precision Interval Arithmetic, Principal Investigator, \$10,000, February 1977 through August 1977.

National Science Foundation Grant MCS76-01661, Development of a Systems Implementation Language for Realizing virtual Systems Principal Investigator, \$35,000. August 1976 through August 1978. (Collaborative research was conducted with T. G. Lewis (Oregon State University) on NSF Grant MCS76-20710, \$65,000, August 1976 through August 1978).

U. S. Army Corps of Engineers DACA39-76-M-0249, Implementation and Benchmarking of an Interval Arithmetic Package on Multics, Principal Investigator, \$8,000. April 1976 through September 1976.

State of Louisiana, Department of Conservation, Natural Resources and Energy Division, Interaction with Federal Data Banks, with M. H. McKinney, W. D. Dominick, and J. Anderson, \$32,700. December 1975 through September 1976.

North Atlantic Treaty Organization Grant 755, Machine Design for Scientific Computation, Principal Investigator, \$9,000. Summers 1974, 1975, 1976, and 1977.

Danish Natural Research Council Grant 511-1546, Research in Dynamically Microprogrammable Systems, Co-Principal Investigator with Peter Kornerup (University of Århus, Denmark), \$69,250. May 1972 through August 1973.

PHD DISSERTATION COMMITTEES CHAIRED

"Model, Graphical Language, and Complexity Metrics for the Design of Software Structure," W. Ingram, University of Louisiana at Lafayette, December, 1988 (Co-chaired with Steve Landry).

"A Dataflow Model Incorporating Condition Handling and Fault Tolerance," D. D. Langan III, University of Louisiana at Lafayette, Spring, 1988. (Co-chaired with Steve Landry).

"List Decomposition: An Application of Scheduling Theory to Program Decomposition with an Emphasis on Communication Costs," J. P. Jeter, University of Louisiana at Lafayette, May 1983.

"A Framework for the Analysis of the Decomposition and Execution of Dataflow Graphs," S. M. Nemecek, University of Louisiana at Lafayette, May 1983. (Co-Chaired with William R. Edwards).

"Dataflow Support for Communicating Asynchronous Processes in a Local Network," L. M. L. Delcambre, University of Louisiana at Lafayette, December 1982.

"A Data-Driven Software Development Language and the Kernel of an Associated Development Methodology," R. E. Michelsen, University of Louisiana at Lafayette, August 1981.

"Interprocess Communications in a Distributed Function Environment Using Dataflow Protocols," E. L. Gallizzi, University of Louisiana at Lafayette, August 1981.

"Systems Oriented Extensions to Dataflow," S. P. Landry, University of Louisiana at Lafayette, May 1981.

"Design and Construction of a Virtual Machine Resource Binding Language," S. Davidson, University of Louisiana at Lafayette, August 1980.

"An Extended Abstract Dataflow Methodology for Designing and Modeling Reconfigurable Systems," V. P. Srimi, University of Louisiana at Lafayette, July 1980.

"A Mathematical Model for a Virtual Machine Monitor and its Supportive Structure," D. Nguyen, University of Louisiana at Lafayette, July 1979.

"Methods of Compacting Microprograms," P. W. Mallet, University of Louisiana at Lafayette, December 1978.

"An Heuristic Approach to Finding Roots of a Polynomial of Any Degree," J. R. Sanders, University of Louisiana at Lafayette, November 1976. (Co-Chaired with Dwight D. Hearn and William R. Edwards).

"A Recursive Virtual Machine Monitor for a Total Virtual Access Architecture," D. M. Hyams, University of Louisiana at Lafayette, August 1976.

"The Design of a Polymorphic Arithmetic Unit: A Case Study of a Multi-Level Computing System," A. L. Lang, University of Louisiana at Lafayette, June 1976.

"Virtual Machine Multiprogramming and Security," L. J. Waguespack, Jr., University of Louisiana at Lafayette, June 1975.

"Multi-Level Interpretative Computing systems: Implications for Hardware Failure Tolerance," L. P. Caillouet, University of Louisiana at Lafayette, May, 1975.

ADDITIONAL PhD COMMITTEE MEMBERSHIP

"A Rule Based Approach to Program Development," A. Birjandi, Oregon State University, May, 1986. (Chaired by Theodore G. Lewis).

"Knowledge-Based Communication and Management Support in a System Development Environment," B. I. Kedzierski, University of Louisiana at Lafayette, December 1983 (Co-Chaired by Cordell Green of Stanford University and William R. Edwards).

"The Specification of a Decompilation Methodology for Data Flow Schema," T. R. Reid, University of Louisiana at Lafayette, November 1978, (Chaired by William R. Edwards).

"A Realization of Relational Semantics in an Automatic Programming System," E. P. Katz, University of Louisiana at Lafayette, Fall 1978, (Chaired by Edwin Towster).

PROFESSIONAL SOCIETIES (Active Membership)

Fellow, Institute of Electronics and Electrical Engineers (**IEEE**):

IEEE Awards Board (1995)

IEEE Technical Activities Board (1992-1993)

Ad Hoc TAB/USAB Committee (1992-1993)

Division VIII Representative to the IEEE Publications Board (1985-1986)

Chairman, Long Range Publications Planning Committee (1986)

Society Publications Committee of the IEEE Publications Board (1985-1986)

Computing Sciences Accreditation Board (CSAB): Visitation Team

Chairman for CSAC (1985-1987)

Representative for Microprogramming in the IEEE Technical Resources Directory, (1983)

Member, **IEEE Computer Society (IEEE CS)**:

Computer Society Representative to IFIP's General Assembly, 1999-2000

Chair, Computer Society International Design Competition, 1997-2000

Chair, Ad Hoc Committee for Special Projects, 1997-2000

Member of the Board of Directors of the Computing Research Association (as the Computer Society representative to the CRA), 1994.

Chair, Board of Governor's Ad Hoc Committee, Central and Eastern European Initiatives Committee, November 1993-1996.

Chair, Federation on Computing in the United States (FOCUS) Board of Directors (serving as one of the Computer Society's representatives to FOCUS), 1993-1996.

Member of the 1994 Planning Committee

Past-President 1993:

Chair of the Intersociety Cooperation Committee

Chair of the Nominations Committee
Member of the Executive Committee and the Board of Governors
Executive Committee liaison to and member of the Fellows Committee
Member of the Planning Committee, Constitution & Bylaws Committee,
Finance Committee, Operations Committee, Personnel & Compensation
Committee.

President, 1992:

The president chairs the Executive Committee, the Board of Governors, the Operations Committee and is a member of the Intersociety Cooperation Committee, Planning Committee, Finance Committee, Operations Committee, Personnel & Compensation Committee.

President Elect, 1991:

The president-elect chairs the Planning Committee, Constitution and By-Laws Committee, Personnel and Compensation Committee, among other committees. Member of the Executive Committee, the Board of Governors, the Operations Committee, Finance Committee.

Executive Committee (1991-1993)

Governing Board (Terms: 1986-1988, 1988-1989, 1990-1991, 1992, 1993)

European Activities Committee (1991-1993)

Asian Activities Committee (1988-1993)

Editor-in-Chief, Computer (1987-1990)

Editor-in-Chief, IEEE Software (1983-1986)

Nominations Committee (1986, 1988, 1991, 1993)

Magazine Financial Planning Committee (1986-1990)

Publications Planning Committee (1986-1990)

Publications Board (1983-1990)

Magazine Advisory Committee (1983-1990)

Advisory Board, COMPUSAT 88 Interactive Satellite Videoconference (1988)

Distinguished Visitors Program (1983-1985)

Joint IEEE CS TC-Micro/ACM SIGMICRO Representative to Euromicro,
(1983, 1984)

Advisor, Technical Committee on Microprogramming Board, (1983-1986)

Chairman, Technical Committee on Microprogramming (1981-1983)

Vice-Chairman, Technical Committee on Computational Medicine (1981-1983)

Vice-Chairman, Technical Committee on Microprogramming (1979-1981)

Special Ad-Hoc Committee on IEEE CS TC Structure & Vitality, (1979-1980)

Associate Technical Editor of **IEEE Computer** (1978-1983)

Member, Association for Computing Machinery (ACM):

Computing Sciences Accreditation Board (CSAB): Visitation Team

Chairman (1985-1987)

Joint ACM SIGMICRO/IEEE TC-Micro Representative to Euromicro (1983, 1984)

Special Interest Group on Microprogramming Board, SIGMICRO, (1977-1980),

Vice-Chairman, SIGMICRO (1977-1979)

ACM National Lecturer (1976-1979, 1984-1985)

Editor, SIGMICRO Newsletter (1975-1977)

International Federation for Information Processing (**IFIP**):
Computer Society Representative to the IFIP General Assembly,
January 1999 to present
Chairman, working Group WG10.1, Systems Concepts and Characteristics
(1986-1990), Member, WG10.1, (1980-1990).

Member, National Council of University Research Administrators

PROFESSIONAL SOCIETIES (Prior Membership)

Member, American Association of University Professors (**AAUP**)

Member, American Mathematical Society (**AMS**)

Member, Society for Industrial and Applied Mathematics (**SIAM**)

CONFERENCE RELATED ACTIVITY

Vice-Chair for XV IFIP World Computer Congress '98, Austria and Hungary, August,
September 1998.

Program Committee, Second International Symposium on High-Performance Computer
Architecture, January 1996.

Program Co-Chairman, Working Conference on Massively Parallel Programming Models,
Berlin, Germany, September 1995.

Program Committee, IEEE International Conference on the Engineering of Complex Computer
Systems, Florida, November 1995.

Program Committee, First International Symposium on High-Performance Computer
Architecture, Raleigh, North Carolina, January 1995.

Program Co-Chairman, Working Conference on Massively Parallel Programming Models,
Berlin, Germany, September 1993.

Program Committee, First IEEE Workshop on Real-Time Applications, New York, May 1993.

Program Committee, Workshop on Parallel and Distributed Real-Time Systems, Newport Beach,
California, April 1993.

Policy Board, Federated Computing Research Conference, 1992.

Program Committee, IFIP WG 10.1 Workshop on Concepts and Characteristics of Declarative
Systems, Budapest, Hungary, October 1988.

Program Committee, IFIP TC-10 Working Conference on Design Methodologies for VLSI and
Computer Architecture, Pisa, Italy, September 1988.

Program Committee, IFIP SG 10.1 Workshop on Concepts and Characteristics of Knowledge-Based Systems, Mt. Fuji, Japan, November 1987.

Technical Program Chair, IBM Research Division Computer Science Technical Symposium, Almaden Research Center, November 1987.

Co-Chairman, Workshop on Object-Oriented Programming, co-sponsored by IBM T. J. Watson Research Center and Brown University, June 1986.

Program Committee and Cluster Coordinator for Programming Languages and Environments, ACIS University Studies Conference, Ft. Lauderdale, Florida, November 1986.

Co-Chairman, IFIP WG 10.1 Workshop on Concepts and Characteristics of Highly Parallel Programming Environments, Bonn, Germany, 1985.

Program Committee Consultant, IFIP TC-10 Working Conference on Fifth Generation Computer Architecture, Manchester, England, 1985.

Program Committee Member, IEEE Workshop on Visual Languages, Hiroshima, Japan, 1984.

ACM/IEEE Microprogramming Workshops:

MICRO-17 (1984), General Conference Chairman, MICRO-16 (1983), Program Committee Member, MICRO-15 (1982), IEEE TC-Micro Representative, MICRO-14 (1981), IEEE TC-Micro Representative, MICRO-11 (1978), Session Coordinator, MICRO-10 (1977), Session Coordinator, MICRO-9 (1976), General Conference Chairman, MICRO-8 (1975), Session Coordinator, MICRO-7 (1974), Session Coordinator, MICRO-6 (1973), Session Coordinator, MICRO-3 (1970), Local Arrangements Committee Member.

General Chairman with R. Sprague, Hawaii International Conference on System Sciences, Honolulu, Hawaii:

HICSS-29 (1996), HICSS-28 (1995), HICSS-27 (1994), HICSS-26 (1993), HICSS-25 (1992), HICSS-24 (1991), HICSS-23 (1990), HICSS-22 (1989), HICSS-21 (1988), HICSS-20 (1987), HICSS-19 (1986), HICSS-18 (1985), HICSS-17 (1984), HICSS-16 (1983), HICSS-15 (1982), HICSS-14 (1981).

Track Chairman, Software Track, Hawaii International Conference on System Sciences:

HICSS-29 (1996), HICSS-28 (1995), HICSS-27 (1994) with H. El-Rewini, HICSS-26 (1993) with H. El-Rewini and T. Lewis, HICSS-25 (1992), HICSS-24 (1990), HICSS-23 (1990), HICSS-22 (1989), HICSS-21 (1988), HICSS-20 (1987), HICSS-19 (1986), HICSS-18 (1985).

Track Chairman, Medical Information Processing Track, Hawaii International Conference on System Sciences:

HICSS-17 (1984) with T. Cousins and T. Walker, HICSS-16 (1983) with T. Walker and T. Cousins, HICSS-15 (1982) with T. Walker and R. Grams, HICSS-14 (1981) with T. Walker and R. Grams, HICSS-13 (1980), HICSS-12 (1979).

Program Chairman, IFIP TC-10 Working Group 10.1 Conference on Methodologies for Computer Systems Design, Lille, France (1983).

Conference Chairman, IEEE First International Conference on Computer Systems Organization, New Orleans, Louisiana (1983).

Program Committee Co-Chairman (with J. Prewitt, R. Grams and T. Lincoln), MEDCOMP '83, IEEE Computer Society - Stocker Symposium on Computers, Information, Systems and Technology Policy in Medicine and Health Care Through 1980's, Athens, Ohio (1983).

Program Committee Member, IFIP TC-2 Working Conference on Systems Descriptions Methodologies, Kecskemet, Hungary (1983).

Program Committee Member, Sixth IEEE International Symposium on Computer Arithmetic, Århus, Denmark (1983).

Conference Co-Chairman (with J. Prewitt), MEDCOMP '82, First IEEE Computer Society International Conference on Computer Medicine and Medical Computer Science, Philadelphia, Pennsylvania (1982).

Program Committee Member, IFIP Working Conference on Firmware, Micro-programming and Restructurable Machines, Linz, Austria (1980).

General Chairman and Program Chairman, L'Exposition de l'Informatique en Louisiane, Distributed Systems based on Mini and Micro Computers, Lafayette, Louisiana (1979).

Co-Director with T. Lewis, the Problems of the 1980's Meeting, Portland, Oregon (1978).

Program Committee Member, Fourth IEEE International Symposium on Computer Arithmetic, Los Angeles, California (1978).

Chaired Sessions or Panels at:

Micro-16, Legal Issues Relating to Firmware (1983)

Multiple Functional Unit Processors Session at the 10th Annual International Symposium on Computer Architecture (1983)

COMPSAC '81

Critical Issues in Computational Medicine Panel (1981), HICSS-14

Special Invited Session on Higher Level Microprogramming Languages and Optimization National Computer Conference (1981)

IFIP Working Conference on Firmware, Microcode and Restructurable Hardware (1980)

Virtual Computer Systems at HICSS-12 (1979)

Special Invited Session on Microcode Correctness, Optimization and Specification, National Computer Conference (1978).

INVITED COLLOQUIUMS, TUTORIALS, PLENARY, and KEYNOTE ADDRESSES

Domestic:

ACM, 23rd Annual Conference of the Southeast Region, Keynote Address
American Academy of Otolaryngology (AAO, New Orleans)
Association of the Bar of the City of New York,
 The Internet & the Web Forum, Keynote Address
 The Cryptography Forum, Keynote Address
Auburn University
Bell Labs
Clemson University
College of William and Mary
Compaq Computer Corporation
Conference on Louisiana Colleges and Universities
Courant Institute of Mathematics
Dartmouth College
DoD Software Technology Strategy Public Forum
HICSS-32, Hawaii International Conference on System Sciences, Plenary Address
HICSS-30, Hawaii International Conference on System Sciences, Plenary Address
IBM AIX Leadership Conference, Dallas
IBM, Boulder, Colorado
IBM, GPD Microcode Symposium, Challenge '82
IBM, San Jose, California
IBM, Thomas J. Watson Research Center
Illinois Institute of Technology
Japan-American Institute of Management Sciences, Keynote Address
Louisiana State University
MEDCOMP '82, Keynote Address
MICRO-16, The 16th Annual Workshop on Microprogramming, Keynote Address
NCR, World Headquarters
Northeastern University (Boston, MA)
Northwestern University (Evanston, IL)
OCATE - Oregon Advanced Technology Education
Oregon Graduate Center
Oregon State University
Purdue University
Rochester Institute of Technology
Rensselaer Polytechnic Institute
Southern Methodist University
State University of New York at Buffalo
Stevens Institute of Technology
Texas A & M University
The Sixth Chautauqua Conference
Seventh Annual IEEE International Conference & Workshop on the
 Engineering of Computer Based Systems, Keynote Address
Trinity University
United States Military Academy, West Point
University of California at Berkeley
University of Colorado
University of Hawaii
University of Massachusetts, Boston

University of New Hampshire
University of Pennsylvania
University of Southern Mississippi
University of Louisiana at Lafayette Honors Convocation, Keynote Address
University of Louisiana at Lafayette Phi Kappa Phi Initiation, Keynote Address
University of Wisconsin
Virginia Polytechnic Institute and State University.

International:

Architectural and Arithmetic Support for Multimedia Seminar (1998) at Dagstuhl
Schloss Wadern, Germany, Keynote Address
Advances in Parallel and Distributed Computing (APDC '97), Shanghai, China
Keynote Address
Brown Boveri Research Labs, Switzerland
Canadian Information Processing Society, Victoria Chapter, Canada
CERT, France
Compsac '91, Tokyo, Japan, Keynote Address
Computer Society of the Republic of China, Taiwan
Danet, Germany
Dansk Tekniske Højskole, Denmark
Des Starlet Kolloquiums, Berlin Germany
East China Institute of Computer Technology, (ECI), Shanghai, China
Ecole Supérieur D'Electricité, France
EurOpen Distributed Open Systems Conference, Norway, Keynote Address
Gesellschaft fuer Informatik Annual Meeting, Dresden, Germany, Keynote Address
GMD Research Institute for Computer Architecture and Software
Technology, Berlin, Germany
Hong Kong University of Science and Technology
IBM Hursley Laboratory, England
IBM Tokyo Research Laboratory, Japan
Industrial Technology Research Institute, Taiwan
Institute for Information Industry, Taiwan
James Cook University, Australia
Keio University, Japan
Kyoto University, Japan
Languages a Machines Cadencees par Les Donnes, France
MICRO-32, The 32nd Annual IEEE/ACM International Symposium on
Microarchitecture, Haifa, Israel, Keynote Address
OKI Electric Industry Co., Ltd, Japan
Politecnico di Torino, Italy
National Center for Information and Documentation, Bulgaria
NEC Central Research Laboratory, Japan
NTT Musashino R&D Center, Japan
Queens College, England
Siemens/Munich, Germany
Technical University of Berlin, Germany
University College, Australian Defense Force Academy, Australia
University of Århus, Denmark

University of Copenhagen, Denmark
University of Dortmund, Germany
University of Edinburgh, Scotland
University of Electro-Communications, Japan
University of Hiroshima, Japan
University of Hong Kong Architecture Workshop
University of Hong Kong Workshop on Information Systems
University of Odense, Denmark
University of Paris, France
University of Queensland, Australia
University of Tokyo, Japan
University of Tromsø, Norway
Workshop on Computer Architecture Education at ISCA, 1998, Barcelona, Spain

ADDITIONAL PROFESSIONAL ACTIVITIES

Industry and Government Related:

Office of Technology Assessment, U.S. Congress, The Electronic Enterprise,
Opportunities for American Business and Industry Project, March 1993
Governor's Task Force on Alternative Futures for Hawaii, 1989
IBM Executive Tools Council, July, 1986-1988
Technical Advisory Committee, NCR's Human Interface Center, 1989
Advisory Committee, Pacific Telecommunications Conference, 1988
Programming Languages and Environments Cluster Chair, IBM ACIS
University Study Conference, 1986
Technical Coordinator, IBM T. J. Watson Research Center
Object-Oriented Programming Workshop, 1986
Experimental Research in Computer Systems IBM Task Force, 1985
IBM Future Computer Systems Lecture Series, 1984-1985

University Related:

Computer Science Department Committees:

Colloquium and Special Events Committee
Executive Committee
Laboratory and Facilities Committee
Graduate Studies Committee
Library Committee
International Microprogramming Repository Committee

University Committees:

University Council
ULL Research Park Committee
Graduate Council
Distinguished Professor Selection Committee
Ad Hoc Presidential Research Institute Formation Committee

Reviewing:

National Science Foundation, **IEEE Transactions on Computers**, **ACM Computing Surveys**, **Communications of the ACM**, **IEEE Software**, **IEEE Computer**, a large

number of IEEE, ACM, AFIP, and IFIP conferences and workshops. Manuscript reviews in computer science topics for several major publishing houses. External tenure and promotion committees for various universities. Project and product development review for various industrial firms.

Miscellaneous:

Developed summer job programs with a variety of firms so both undergraduate and graduate students could have industrial learning experiences. Assisted in fund raising activities and in establishing scholarships. Formed committee structure for the computer science department.

AWARDS AND HONORS

IEEE Richard E. Merwin Distinguished Service Award, 2002, for “For outstanding service to both the Computer Society and computing profession that continues to have enormous impact on responsible governance, high-quality publications and conferences, and the international community.””

IEEE Third Millennium Medal, 2000.

IEEE Computer Society Golden Core Member, 1996.

IEEE Past Presidents 50th Anniversary Gavel Presentation, 1996.

IEEE Computer Society Distinguished Service Award, 1994.

IEEE Computer Society Distinguished Service Award, 1993.

Eminent Scholar Chair of Computer Science, University of Louisiana at Lafayette, 1989-1991.

IEEE Fellow (cited for contributions to computer systems organization and microprogramming), 1990.

IEEE Computer Society Outstanding Contribution Award, 1990.

Henry A. Walker, Jr. Distinguished Professor Chair, University of Hawaii, 1988-89.

IEEE Computer Society Distinguished Service Award, 1988.

IEEE Computer Society Outstanding Contribution Award, 1987.

IEEE Software, Certificate of Appreciation, 1987.

IEEE Computer Society Meritorious Service Award, 1985.

Phi Kappa Phi Honor Society, Inducted, 1984.

IEEE Senior Member, 1984-1989.

IEEE Computer Society Certificate of Appreciation, 1983.

Alfred Lamson Research Professor of Computer Science, ULL, 1981-1984.

University of Louisiana at Lafayette Foundation Professorship, 1981.

University of Louisiana at Lafayette Distinguished Professor Award, 1978.

Member, Upsilon Pi Epsilon, National Computer Science Honor Society, 1975.

National Science Foundation Fellow 1969-70, 1970-71.

Sophie Cubbison Award, California State Polytechnic Alumni Association, 1963.

AIAA Minta Martin Certificate of Merit, 1963.

AIAA Student Branch Scholastic Award, 1963.

Member, Sigma Pi Alpha, Engineering Honor Fraternity, 1962.

Solar Aircraft Company Scholarship, California State Polytechnic University, 1961.

President's List and Dean's List, California State Polytechnic University, 1960-1963.